

FIG. 1 is a block diagram of a system architecture.

1

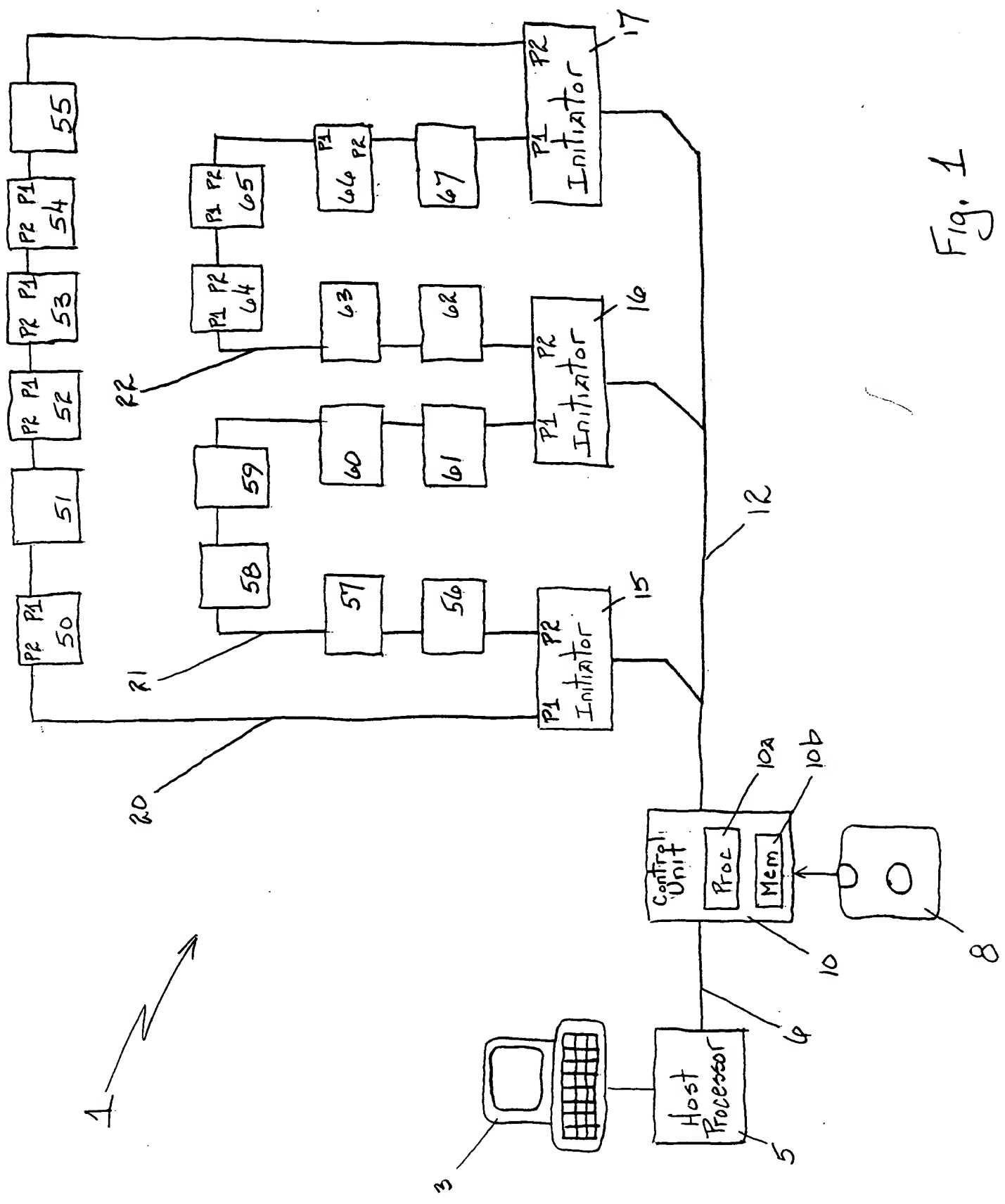


Fig. 1

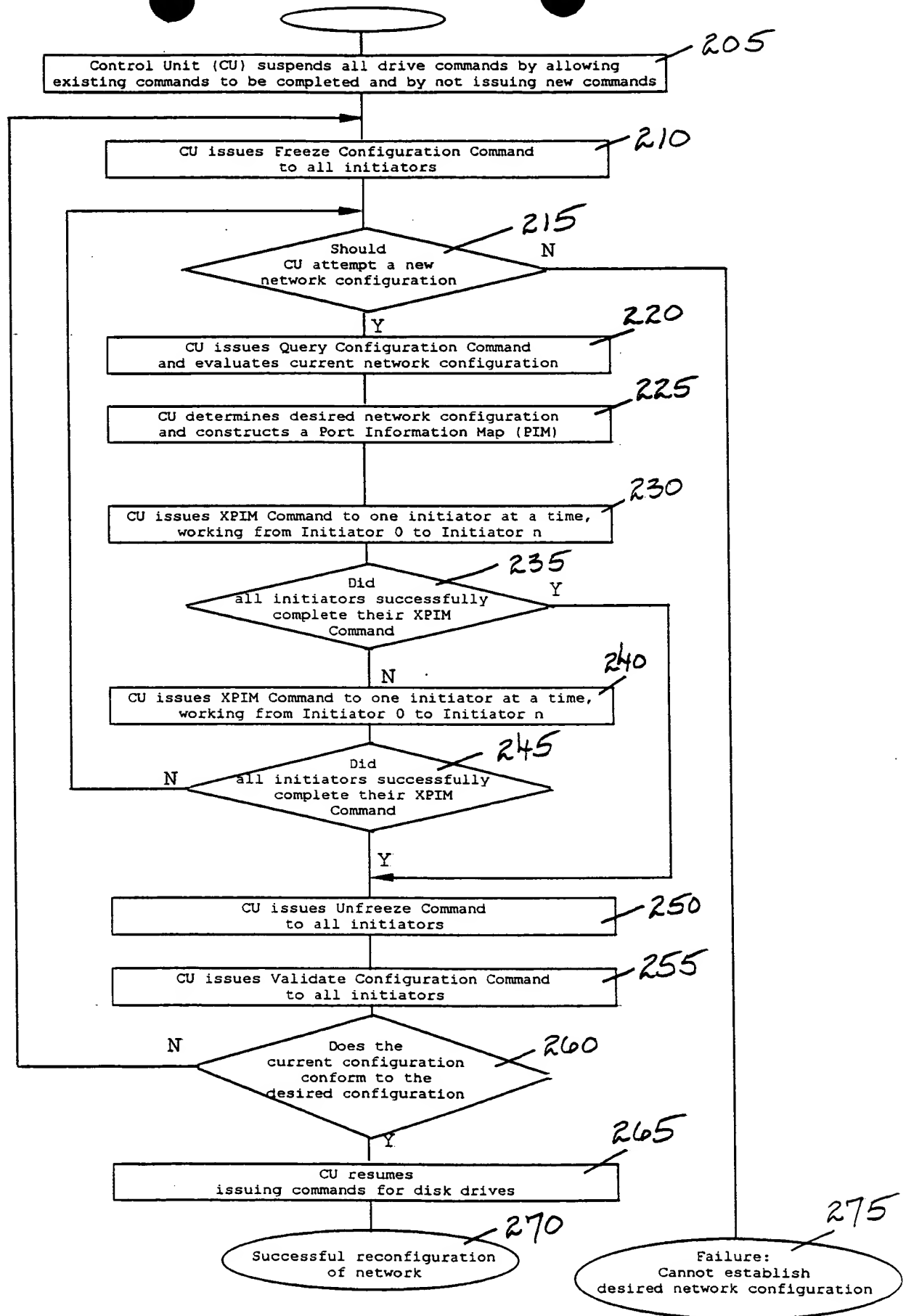


Fig. 2

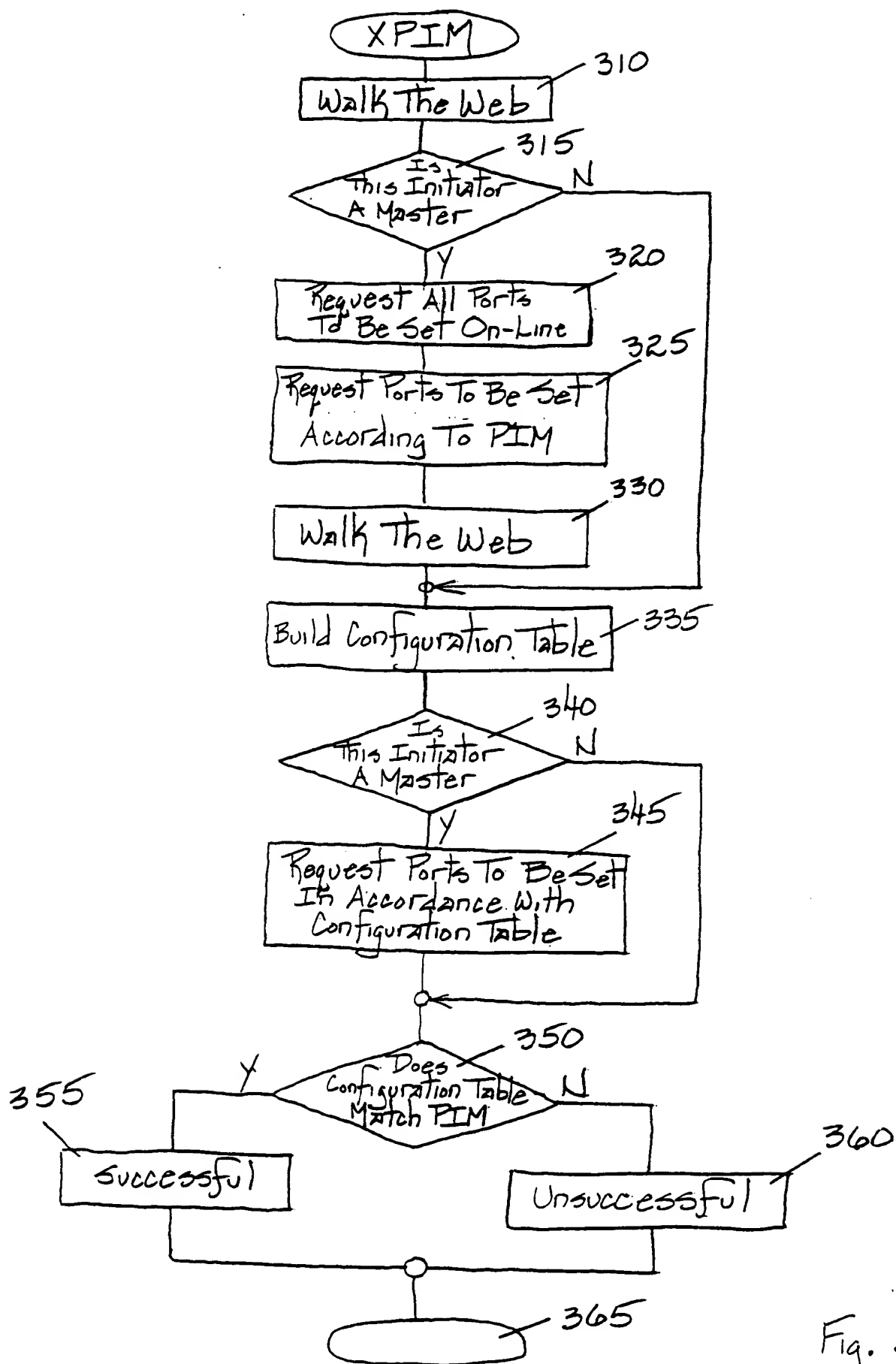


Fig. 3

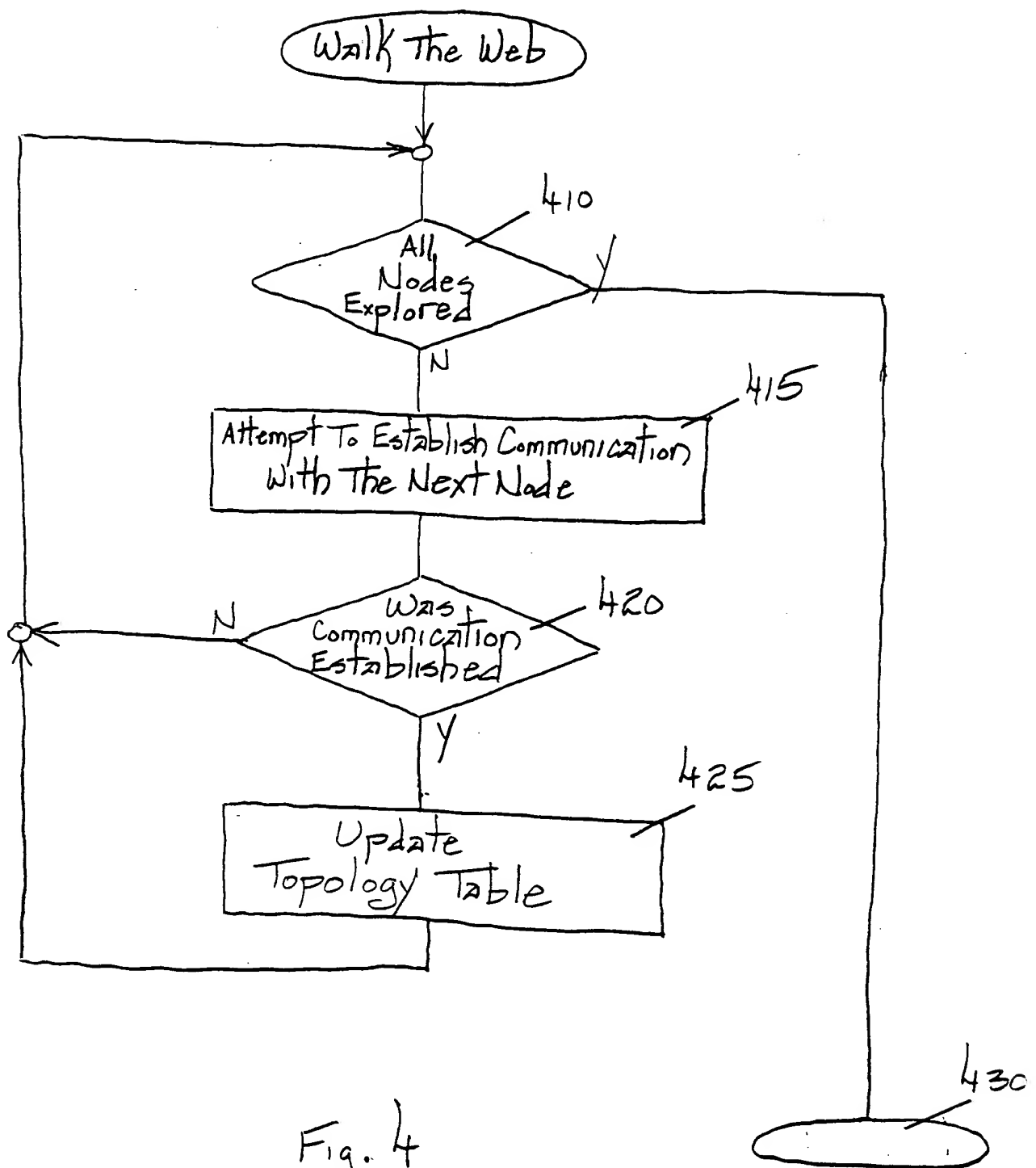


Fig. 4